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Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Timothy B. Cowles et al.

Attorney Docket No.: 500792.03

Serial No. : 10/043,680

Group Art Unit : 2824

Filed : January 10, 2002 Examiner

: Van Thu T. Nguyen

Confirmation No.

: 9219

Date of Notice

of Allowance

: January 15, 2003

Title

: REFRESH CONTROLLER AND ADDRESS REMAPPING CIRCUIT AND METHOD

FOR DUAL MODE FULL/REDUCED DENSITY DRAMS

Box Issue Fee Commissioner of Patents Washington, DC 20231

AMENDMENT AFTER ALLOWANCE (UNDER 37 C.F.R. § 1.312)

Sir:

Applicants acknowledge receipt of the Notice of Allowance dated January 15.

2003.

In the Specification:

Please replace the paragraph beginning at page 16, line 26, with the following rewritten paragraph:

As mentioned above, the column address buffers 34 are designed to remap one of the row address to a column address so that the SDRAM 20 is plug compatible with SDRAMs adapted to receive row addresses for M rows and column addresses for N/2 columns. With

DORSEY & WHITNEY LLP

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SAN FRANCISCO

ANCHORAGE

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U.S. BANK CENTRE 1420 FIFTH AVENUE, SUITE 3400 FAX RECEIVED

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COMMENTS: FURTHER TO OUR CONVERSATION, PLEASE FIND ATTACHED A COPY OF THE AMENDMENT AFTER ALLOWANCE AND A COPY OF THE DATE STAMPED POST CARD SHOWING THAT THE AMENDMENT WAS RECEIVED BY THE PTO ON FEBRUARY 13, 2003.

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Commissioner for Patents Washington, DC 20231

SENT: February 7, 2003

DUE: N/A

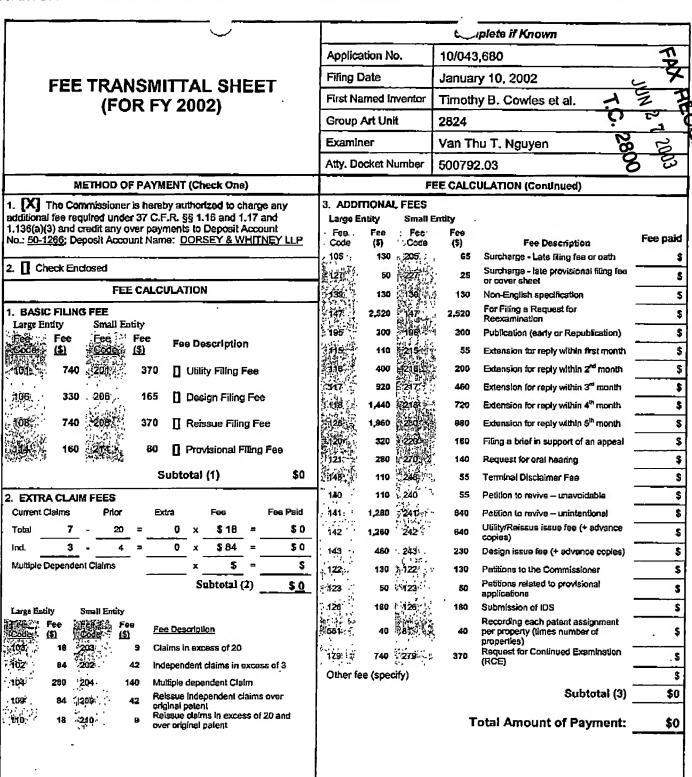
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Fee Transmittal Sheet (+ copy); Amendment After Allowance; in re: Timothy B. Cowles et al., USAN 10/043,680, filed January 10, 2002, allowed January 15, 2003, for REFRESH CONTROLLER AND ADDRESS REMAPPING CIRCUIT AND METHOD FOR DUAL MODE/FULL REDUCED DENSITY DRAMS.

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